

FIG._1

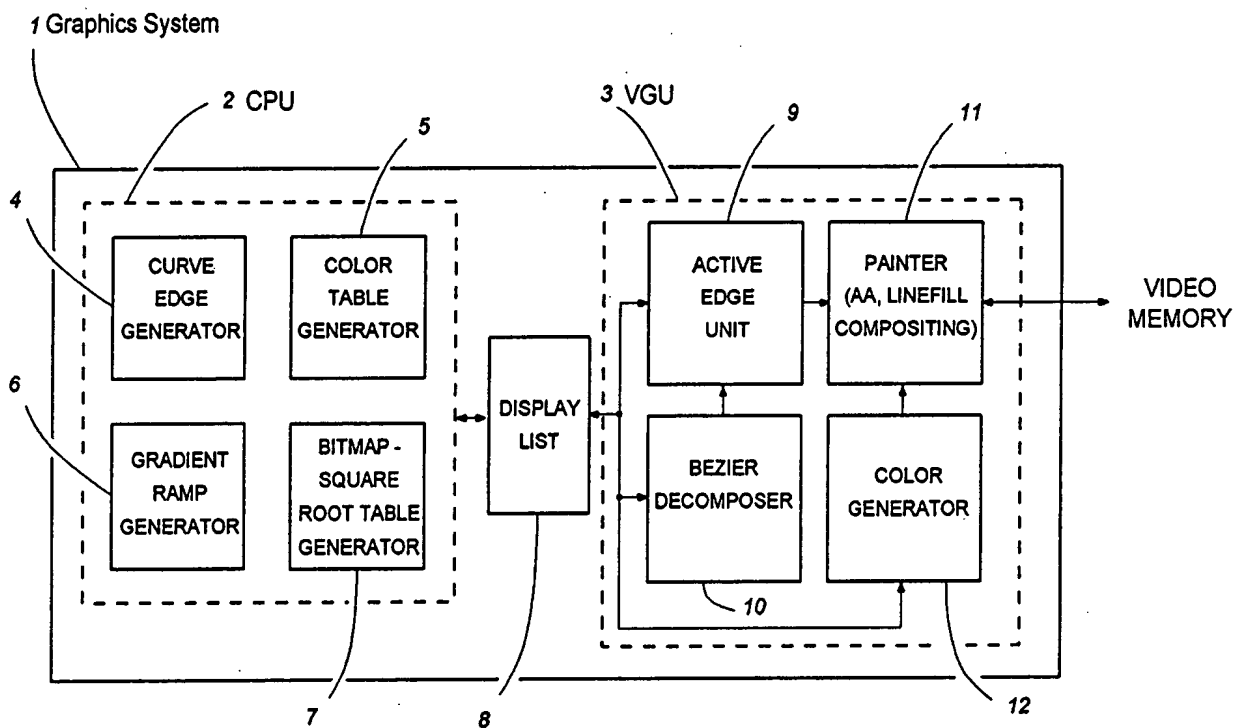


FIG._2

Express Mail #EV 14084782905

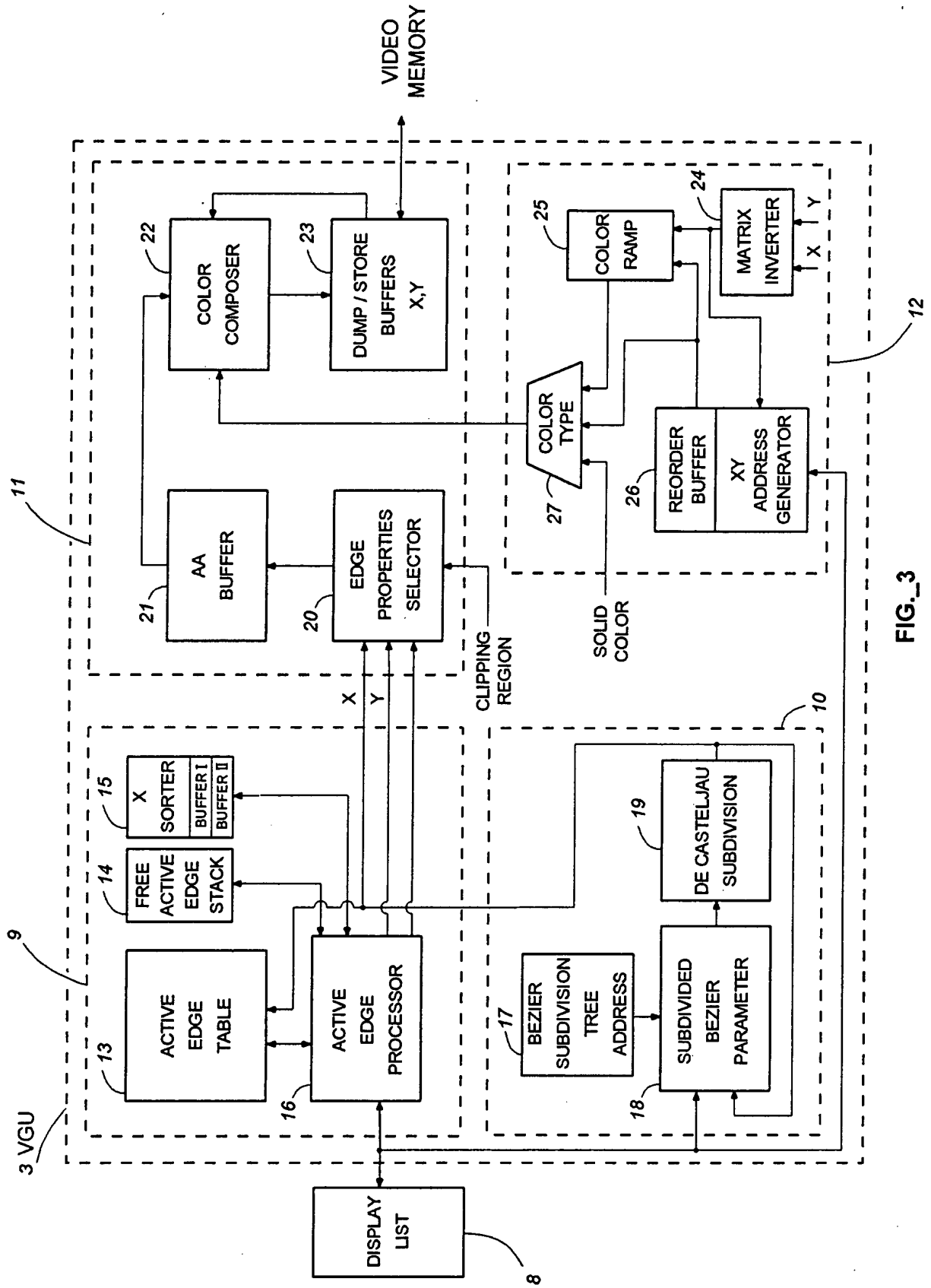


FIG._3

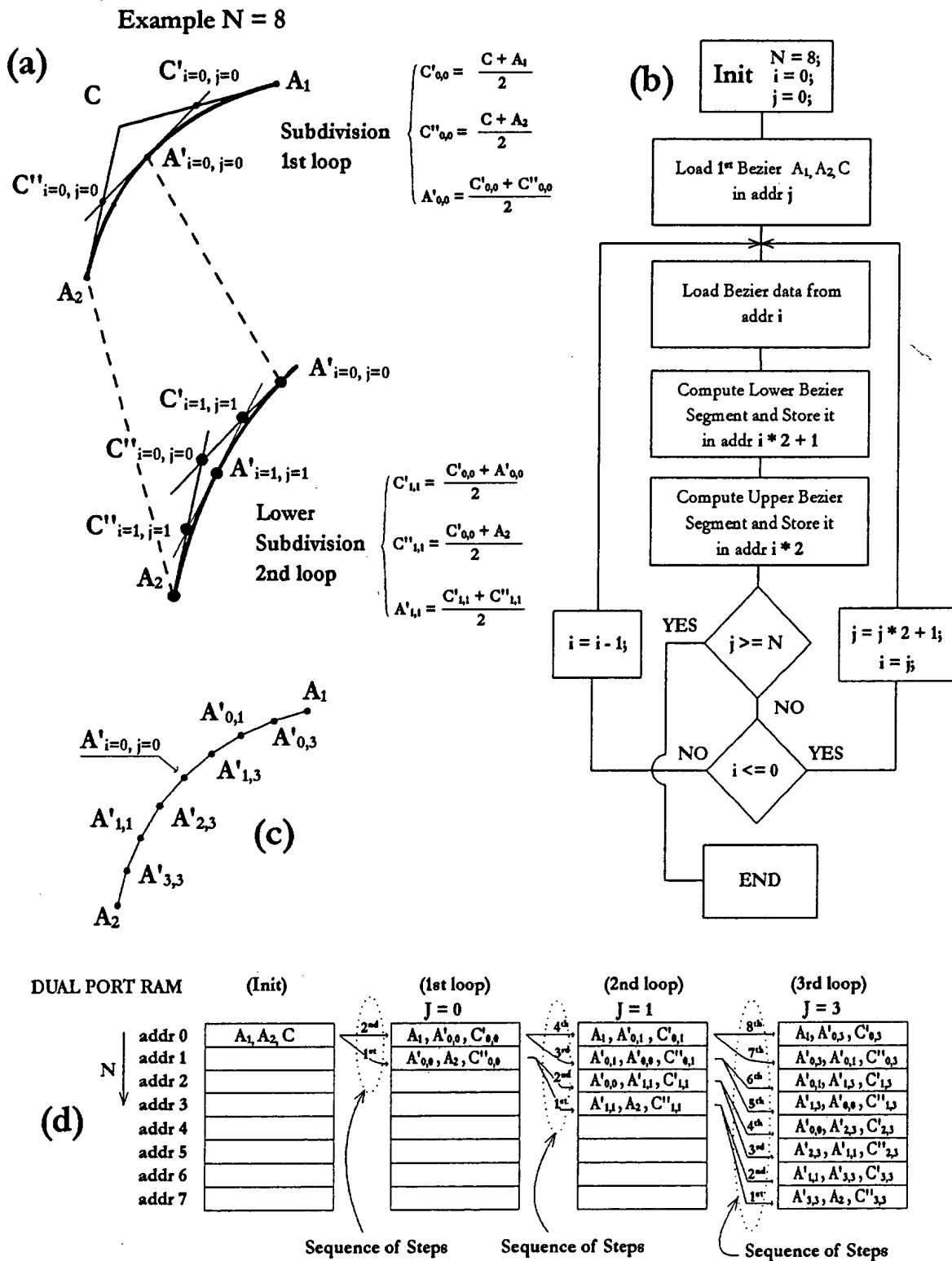


FIG. 4

Free Active Edge Stack (LIFO Stack Memory)

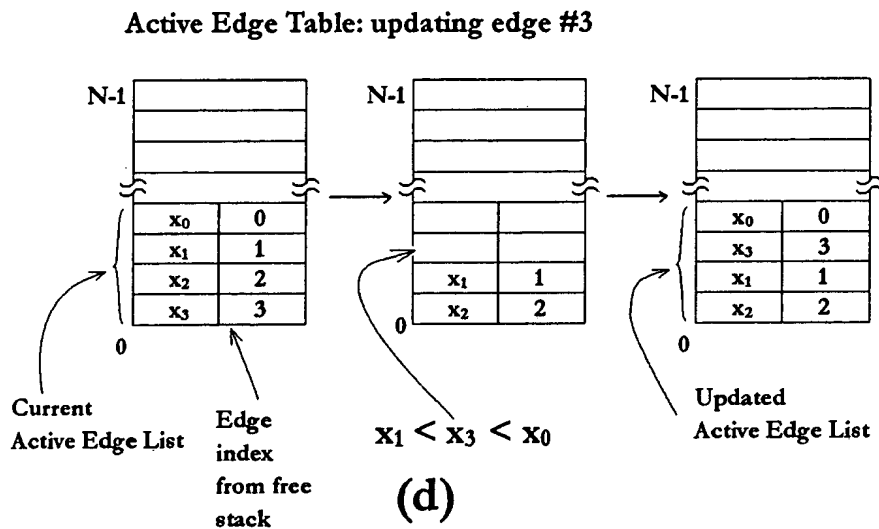
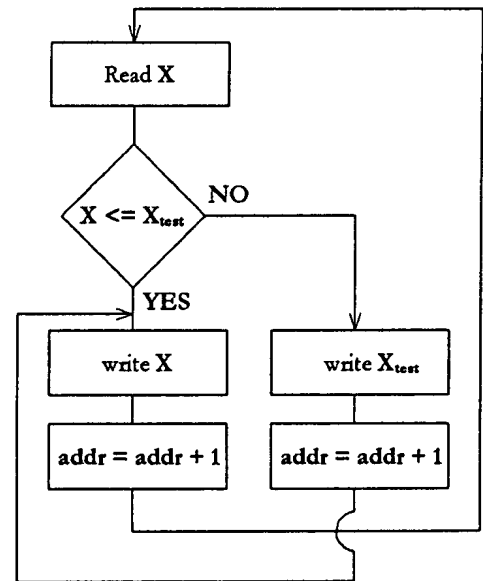
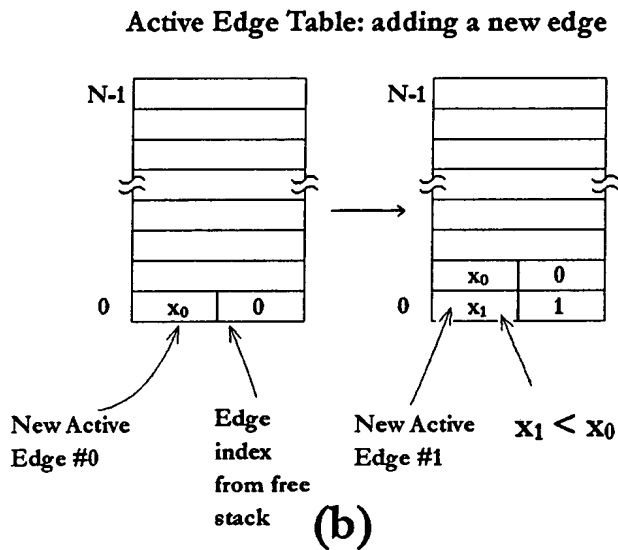
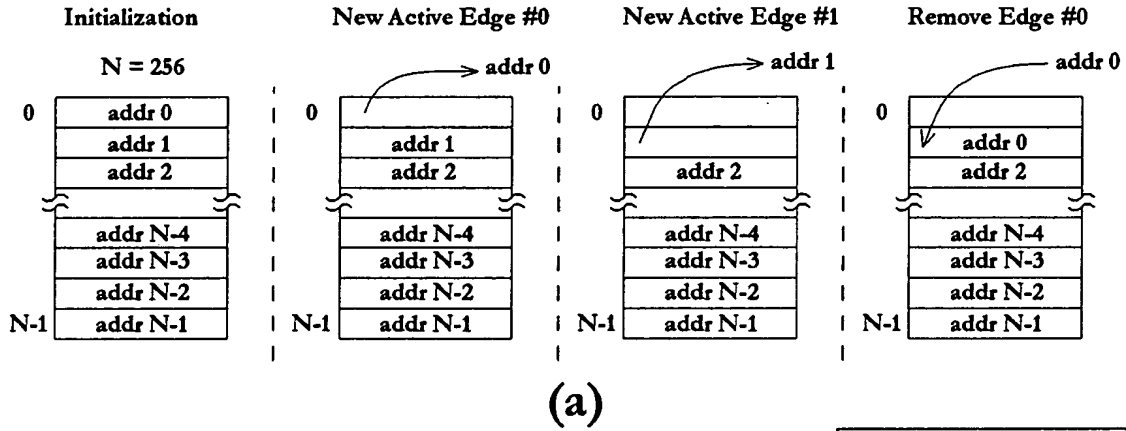
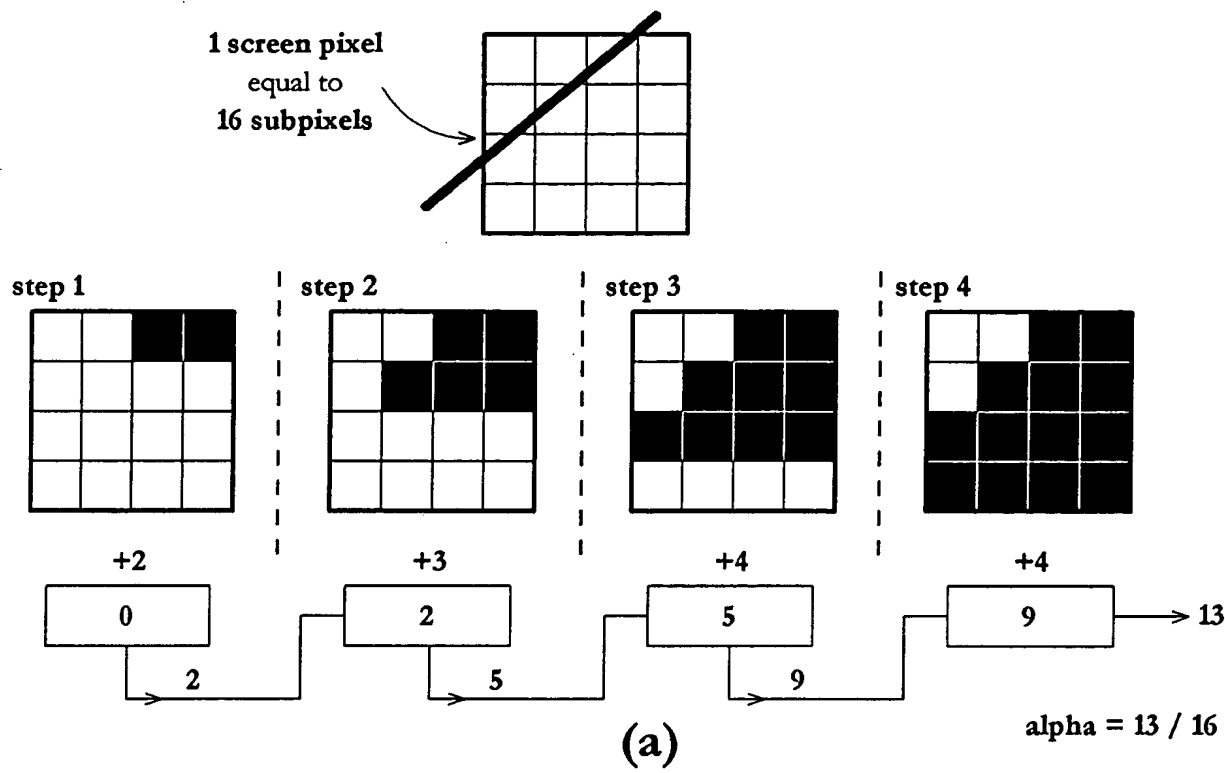
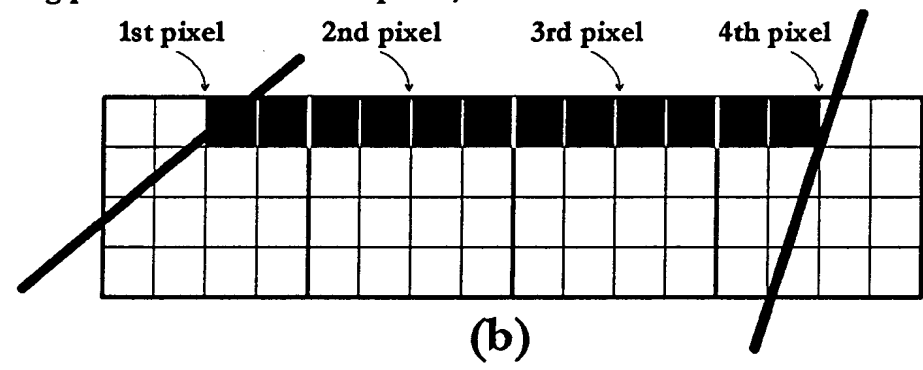


FIG. 5



Anti aliasing parallism with $N = 4$ pixels;



Init - End inside the same sub-pixel matrix

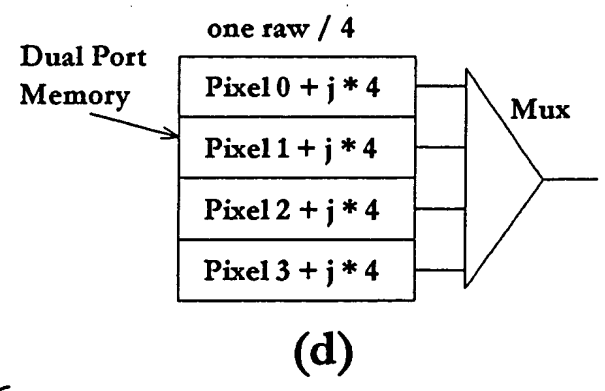
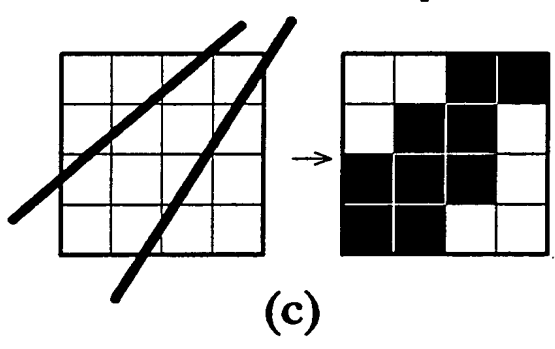


FIG. 6

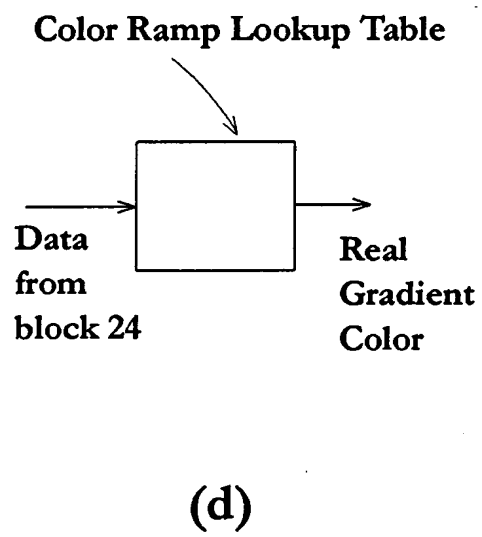
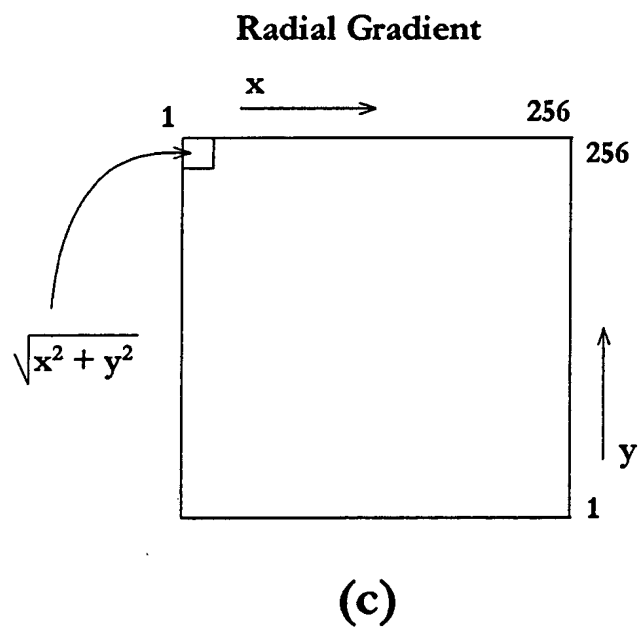
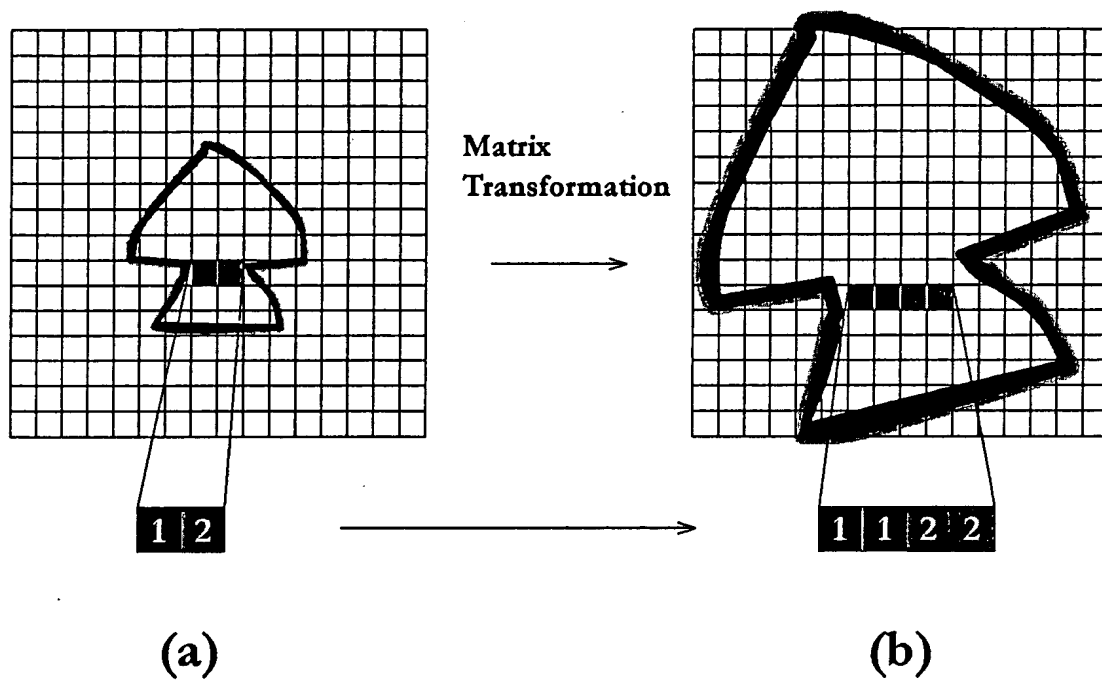
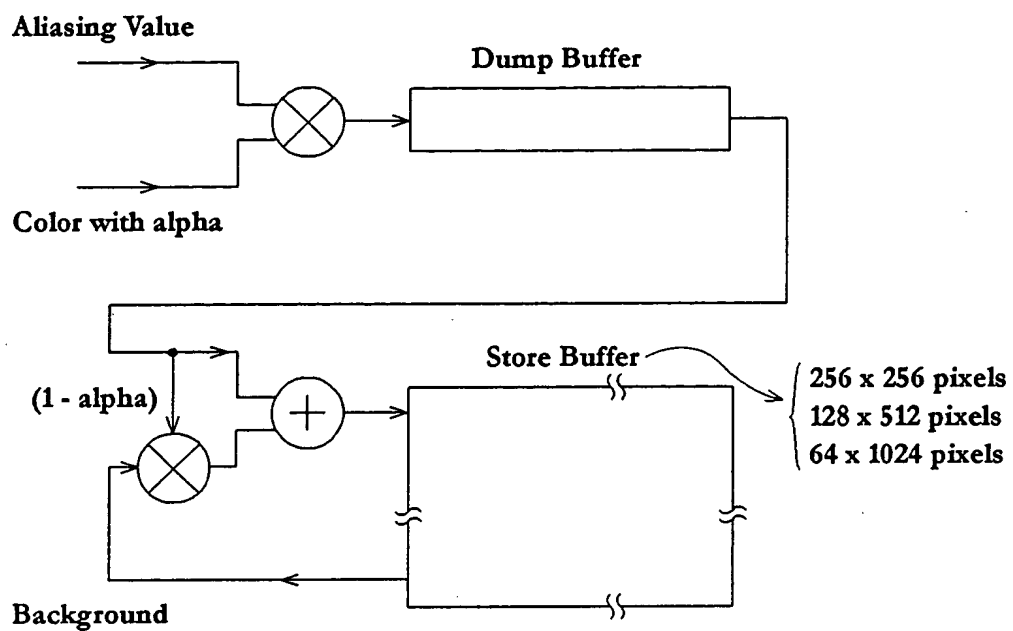
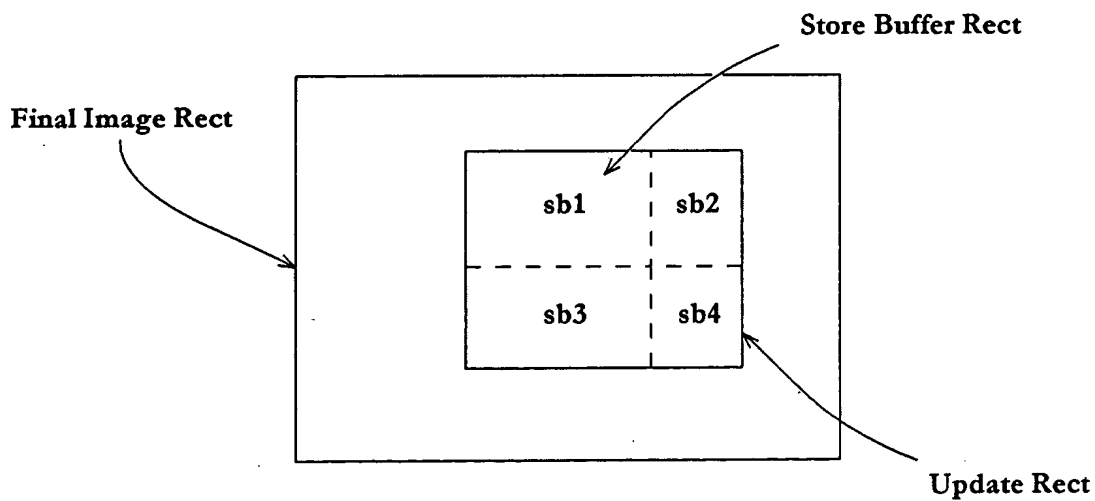


FIG. 7



(a)



(b)

FIG. 8